FIG.1

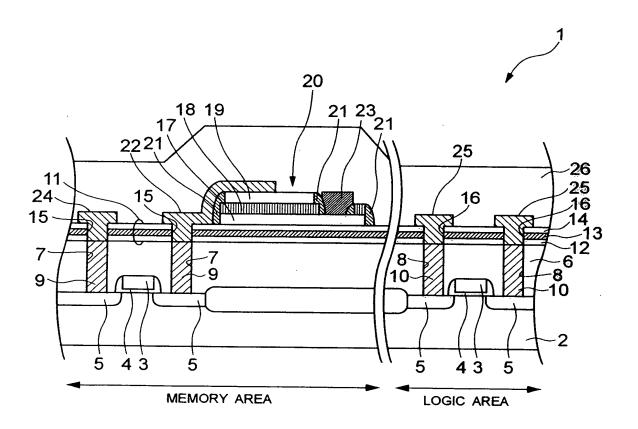




FIG.2

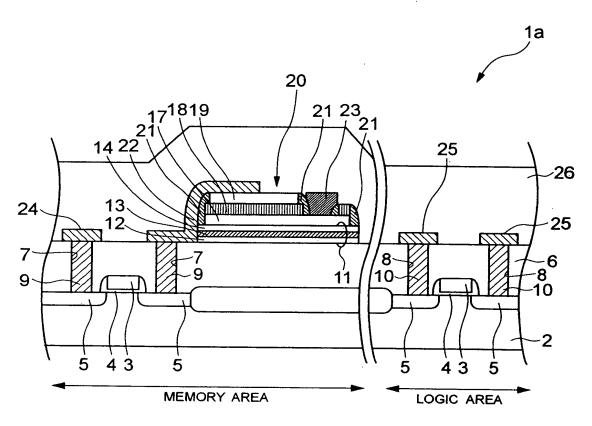


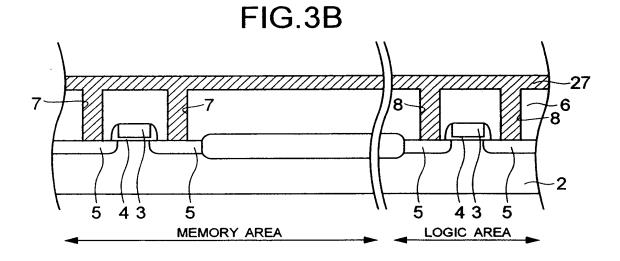
FIG.3A

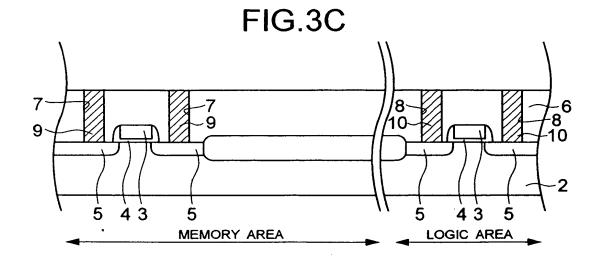
6

5 4 3 5

MEMORY AREA

LOGIC AREA





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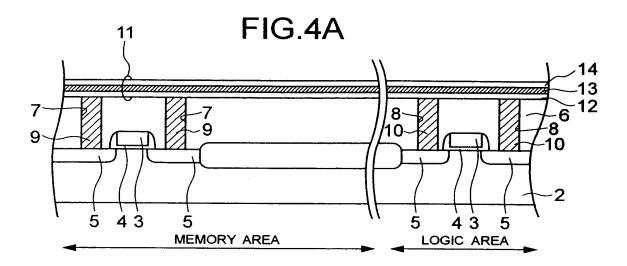


FIG.4B

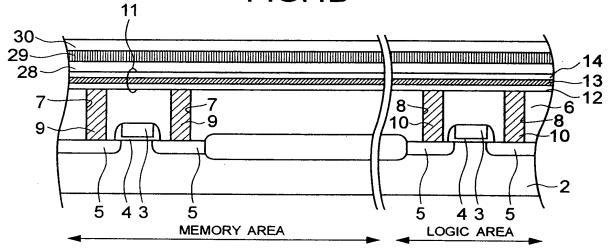


FIG.4C

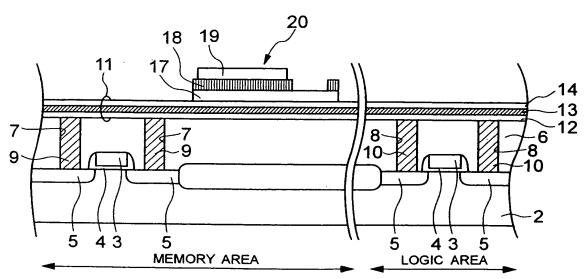


FIG.5A

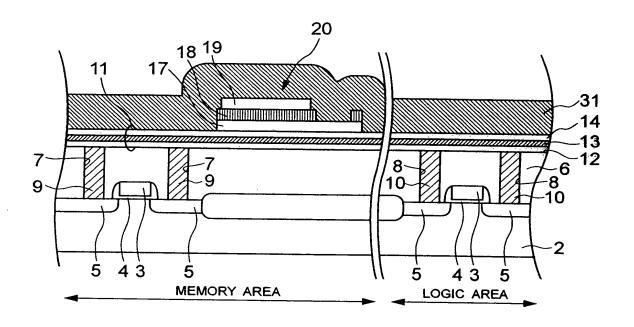


FIG.5B

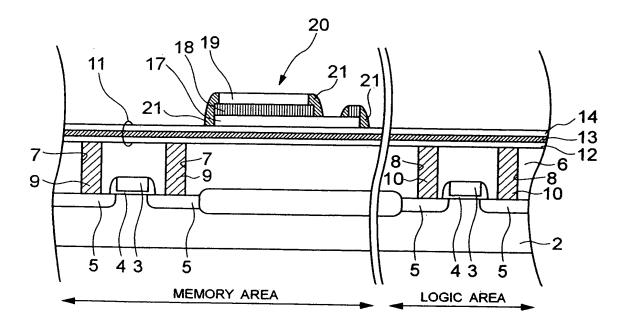


FIG.6A

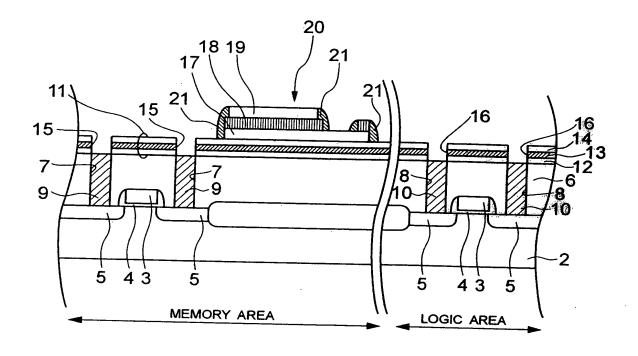


FIG.6B

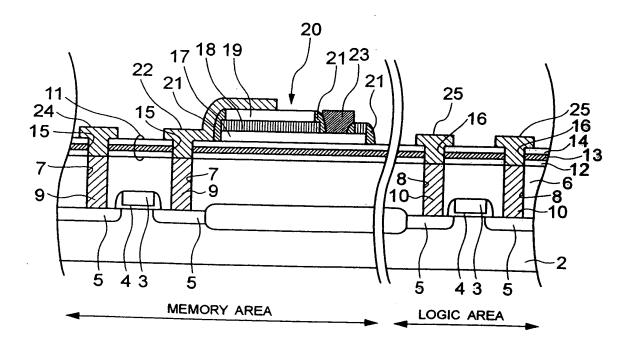


FIG.7

